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#### NOTICE OF ALLOWANCE AND FEE(S) DUE

23696 7590 11/30/2010 QUALCOMM INCORPORATED 5775 MOREHOUSE DR. SAN DIEGO, CA 92121 EXAMINER

SAFAIPOUR, BOBBAK

ART UNIT PAPER NUMBER

2618

DATE MAILED: 11/30/2010

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/650,564	08/27/2003	Messay Amerga	020673	8247	

TITLE OF INVENTION: INTRA-FREQUENCY SEARCHING IN THE PRESENCE OF FREQUENCY GAPS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(8) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	02/28/2011

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT, PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

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APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTO	RNEY DOCKET NO.	CONFIRM	MATION NO.
10/650,564	08/27/2003		Messay Amerga			020673	-	8247
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APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE	FEE	TOTAL FEE(S) DUE		ATE DUE
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SAFAIPOUI	-	2618	455-516000					
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10/650,564	08/27/2003	Messay Amerga	020673	8247			
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QUALCOMM II	NCORPORATED	SAFAIPOUR, BOBBAK					
S775 MOREHOUSE DR. SAN DIEGO, CA 92121			ART UNIT PAPER NUMB				
			2618				

# Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 697 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 697 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

# Application No. Applicant(s) 10/650,564 AMERGA, MESSAY Notice of Allowability Examiner Art Unit BORBAK SAFAIPOUR 2618 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. This communication is responsive to 11/12/2010. 2. The allowed claim(s) is/are 1 and 3-21. 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). b) \( \subseteq \text{Some\*} \) c) \( \subseteq \text{None} \) of the: a) $\square$ All 1. T Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). \* Certified copies not received: \_\_\_\_\_. Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) Including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. Attachment(s) 1. Notice of References Cited (PTO-892) 5. Notice of Informal Patent Application 2. Notice of Draftperson's Patent Drawing Review (PTO-948) Interview Summary (PTO-413), Paper No./Mail Date 3. Information Disclosure Statements (PTO/SB/08), 7. X Examiner's Amendment/Comment Paper No./Mail Date 4. T Examiner's Comment Regarding Requirement for Deposit 8. X Examiner's Statement of Reasons for Allowance of Biological Material 9. ☐ Other . /Bobbak Safainour/

Examiner, Art Unit 2618

#### EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Ramin Mobarhan, Reg. No. 50,182 on Friday, November 12, 2010.

The application has been amended as follows:

1. (Currently Amended) An apparatus, comprising:

a search scheduler for scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and for generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell; and

a frequency controller for generating frequency switch commands, receiving the frequency switch blocking signal, and suppressing the generation of frequency switch commands when the frequency switch blocking signal is asserted; and

a gap manager for indicating when a frequency switch is to occur, and wherein the search scheduler schedules the search during a period of time without the frequency switch as indicated by the gap manager.

## (Canceled).

3. (Original) The apparatus of claim 1, wherein the search scheduler comprises a timer, the

expiration of which indicates a search is to be scheduled.

4. (Original) The apparatus of claim 3, wherein the search scheduler schedules a search

without asserting the frequency switch blocking signal prior to the timer expiration.

5. (Original) The apparatus of claim 3, wherein the timer resets upon the completion of a

scheduled search.

6. (Original) The apparatus of claim 3, wherein the search scheduler schedules a search and

asserts the frequency switch blocking signal subsequent to the timer expiration.

7. (Original) The apparatus of claim 1, wherein the search scheduler asserts the frequency

switch blocking signal during the scheduled search.

8. (Currently Amended) The apparatus of claim 1, [[2,]] wherein the search scheduler

schedules a plurality of search types.

9. (Original) The apparatus of claim 8, wherein the search scheduler schedules one or more

of the plurality of search types in response to the frequency switch indicator received from the

gap manager.

10. (Original) The apparatus of claim 8, wherein the search scheduler comprises a plurality of timers corresponding to one or more of the plurality of search types, the expiration of each timer indicating a search of the respective search type is to be scheduled.

- 11. (Original) The apparatus of claim 10, wherein the search scheduler schedules a search corresponding to one of the plurality of search types and asserts the frequency switch blocking signal subsequent to the respective timer expiration.
- (Original) The apparatus of claim 8, wherein the plurality of search types comprises one
  or more of a list search, a W-CDMA step one search, or a W-CDMA step two search.
- 13. (Currently Amended) A first Integrated Circuit (IC), responsive to a frequency switch signal generated in a second IC, the second IC comprising:
- a search scheduler for scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and for generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell; and
- a frequency controller for generating a frequency switch signal comprising frequency switch commands, receiving the frequency switch blocking signal, and suppressing the generation of frequency switch commands when the frequency switch blocking signal is asserted; and

a gap manager for indicating when a frequency switch is to occur, and wherein the search scheduler schedules the search during a period of time without the frequency switch as indicated by the gap manager,

the first IC comprising:

a frequency synthesizer to receive the frequency switch signal from the second IC and to generate an output signal, the frequency of the output signal changing from a first frequency to a second frequency in response to the frequency switch signal.

14. (Currently Amended) A wireless communication device, comprising:

a processor for

indicating when a frequency switch is to occur,

scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and during a period of time without the frequency switch;

generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell;

generating frequency switch commands; and

suppressing the generation of frequency switch commands when the frequency switch blocking signal is asserted.

 (Original) The wireless communication device of claim 14, further comprising a frequency synthesizer to receive the frequency switch commands and to generate an output

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signal, the frequency of the output signal changing from a first frequency to a second frequency in response to the frequency switch commands.

- 16. (Original) The wireless communication device of claim 14, further comprising a searcher for searching in accordance with the scheduled search and for indicating to the search scheduler when the scheduled search is complete.
- 17. (Currently Amended) A method of searching in the presence of frequency gaps, comprising:

indicating when a frequency switch is to occur;

scheduling a search based at least in part on a <u>duration</u> during of a serving cell transceiving gap defined for searching outside of a serving cell <u>and during a period of time</u> without the frequency switch; and

generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell;

generating frequency switch commands; and

suppressing the generation of frequency switches switch commands when the frequency switch blocking signal is enabled during the scheduled search to override opening the serving cell transceiving gap for searching outside of the serving cell.

(Original) The method of claim 17, further comprising:
 determining future frequency switches; and

wherein the search is scheduled during a time period in which no future frequency switches are determined

# 19. (Original) The method of claim 18, further comprising:

timing the duration between searches; and

scheduling searches without suppressing frequency switches prior to the timed duration reaching a pre-determined maximum.

# 20. (Currently Amended) An apparatus, comprising:

means for indicating when a frequency switch is to occur;

means for scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and during a period of time without the frequency switch; and

means for generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell;

means for generating frequency switch commands; and

means for suppressing the generation of frequency switches switch commands when the frequency switch blocking signal is enabled-during the scheduled search to override opening the serving cell transceiving gap for searching outside of the serving cell.

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21. (Currently Amended) Processor readable media A storage medium device encoded

thereon with processor-executable instructions for causing a processor to perform the following

steps:

indicating when a frequency switch is to occur;

scheduling a search based at least in part on a <u>duration</u> during of a serving cell

transceiving gap defined for searching outside of a serving cell and during a period of time

without the frequency switch; and

generating a frequency switch blocking signal to override opening the serving cell

transceiving gap for searching outside of the serving cell;

generating frequency switch commands; and

suppressing the generation of frequency switches switch commands when the frequency

switch blocking signal is enabled during the scheduled search to override opening the serving

cell transceiving gap for searching outside of the serving cell.

Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

Claim 2 has been cancelled.

Claims 1 and 3-21 are allowed.

Consider claim 1, the best prior art of record found during the examination of the present

application, Amerga et al (hereinafter "Amerga"; US 2003/0231605) in view of Bamburak et

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al. (US 2004/0219915 A1; hereinafter Bamburak), fails to specifically disclose, teach, or suggest an apparatus, comprising: a search scheduler for scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and for generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell; a frequency controller for generating frequency switch commands, receiving the frequency switch blocking signal, and suppressing the generation of frequency switch commands when the frequency switch blocking signal is asserted; and a gap manager for indicating when a frequency switch is to occur, and wherein the search scheduler schedules the search during a period of time without the frequency switch as indicated by the gap manager.

Claims 3-12 are allowable because it is dependent upon independent claim 1.

Consider claim 13, the best prior art of record found during the examination of the present application, Amerga et al (hereinafter "Amerga"; US 2003/0231605) in view of Bamburak et al. (US 2004/0219915 A1; hereinafter Bamburak), fails to specifically disclose, teach, or suggest a first Integrated Circuit (IC), responsive to a frequency switch signal generated in a second IC, the second IC comprising: a search scheduler for scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and for generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell; a frequency controller for generating a frequency switch signal comprising frequency switch commands, receiving the

frequency switch blocking signal, and suppressing the generation of frequency switch commands when the frequency switch blocking signal is asserted; and a gap manager for indicating when a frequency switch is to occur, and wherein the search scheduler schedules the search during a period of time without the frequency switch as indicated by the gap manager, the first IC comprising: a frequency synthesizer to receive the frequency switch signal from the second IC and to generate an output signal, the frequency of the output signal changing from a first frequency to a second frequency in response to the frequency switch signal.

Consider claim 14, the best prior art of record found during the examination of the present application, Amerga et al (hereinafter "Amerga"; US 2003/0231605) in view of Bamburak et al. (US 2004/0219915 A1; hereinafter Bamburak), fails to specifically disclose, teach, or suggest a wireless communication device, comprising: a processor for indicating when a frequency switch is to occur; scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and during a period of time without the frequency switch; generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell; generating frequency switch commands; and suppressing the generation of frequency switch commands when the frequency switch blocking signal is asserted.

Claims 15-16 are allowable because it is dependent upon independent claim 14.

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Consider claim 17, the best prior art of record found during the examination of the present application, Amerga et al (hereinafter "Amerga"; US 2003/0231605) in view of Bamburak et al. (US 2004/0219915 Al; hereinafter Bamburak), fails to specifically disclose, teach, or suggest a method of searching in the presence of frequency gaps, comprising: indicating when a frequency switch is to occur; scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and during a period of time without the frequency switch; generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell; generating frequency switch commands; and suppressing the generation of frequency switch commands when the frequency switch blocking signal is enabled.

Claims 18-19 are allowable because it is dependent upon independent claim 14.

Consider claim 20, the best prior art of record found during the examination of the present application, Amerga et al (hereinafter "Amerga"; US 2003/0231605) in view of Bamburak et al. (US 2004/0219915 A1; hereinafter Bamburak), fails to specifically disclose, teach, or suggest an apparatus, comprising: means for indicating when a frequency switch is to occur; means for scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and during a period of time without the frequency switch; means for generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell;

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means for generating frequency switch commands; and means for suppressing the generation of frequency switch commands when the frequency switch blocking signal is enabled.

Consider claim 21, the best prior art of record found during the examination of the present application, Amerga et al (hereinafter "Amerga"; US 2003/0231605) in view of Bamburak et al. (US 2004/0219915 A1; hereinafter Bamburak), fails to specifically disclose, teach, or suggest a storage medium device encoded thereon with processor-executable instructions for causing a processor to perform the following steps: indicating when a frequency switch is to occur; scheduling a search based at least in part on a duration of a serving cell transceiving gap defined for searching outside of a serving cell and during a period of time without the frequency switch; generating a frequency switch blocking signal to override opening the serving cell transceiving gap for searching outside of the serving cell; generating frequency switch commands; and suppressing the generation of frequency switch commands when the frequency switch blocking signal is enabled.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to BOBBAK SAFAIPOUR whose telephone number is (571)270-1092. The examiner can normally be reached on Monday - Friday, 8:00 a.m., - 5:00 p.m., EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Anderson can be reached on (571) 272-4177. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Bobbak Safaipour/ Examiner, Art Unit 2618

November 17, 2010

/Matthew D. Anderson/ Supervisory Patent Examiner, Art Unit 2618